	Application No.	Applicant(s)
Notice of Allowability	10/648,195	YANG, PACHINCO
	Examiner	Art Unit
	Christopher A. Daley	2111
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to 10/1/2005.		
2. The allowed claim(s) is/are <u>1-14</u> .		
3.		
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. Interview Summary Paper No./Mail Da 7. Examiner's Amenda 8. Examiner's Stateme 9. Other	te

Application/Control Number: 10/648,195 Page 2

Art Unit: 2111

Allowable Subject Matter

1. Claims 1 – 14 are allowed.

2. The following is a statement of reasons for the indication of allowable subject

matter:

3. Independent claim 1 is allowable over the prior art of record because the

examiner found neither prior art cited in its entirety, nor based on the prior art found any

motivation to combine any of said prior art. The following limitations were not found in

the prior art of record:

a memory controller to enable the CPU, under the memory controller's control, to fetch

the program instruction and access the data in the set of memory modules, the

memory controller also comprising a first re-addressing;

wherein when the interruption occurs, the CPU generates an interrupt vector address

to the memory controller, and when the re-addressing device of the memory controller

identifies that the address falls within the address range of the interrupt vector table,

the re-addressing device sends out an enable signal to the high-speed memory

enabling the CPU to fetch the corresponding entry instruction of the interrupt service

routines stored in the high-speed memory, instead of the predetermined low-speed

memory, so as to reduce the interrupt latency when fetching the program instruction.

Dependent claims 2 – 14 are rendered allowable, as associated independent claim 1 is

allowable.

Application/Control Number: 10/648,195 Page 3

Art Unit: 2111

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher A. Daley whose telephone number is 571 272 3625. The examiner can normally be reached on 9 am. - 4p m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571 272 3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CAD 12/08/05

TIM VO PRIMARY EXAMINER